

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Shannon V. Davidson et al.  
Serial No.: 10/825,345  
Filing Date: April 15, 2004  
Group Art Unit: 2161  
Confirmation No.: 8660  
Title: System and Method for Computer Cluster Virtualization Using  
Dynamic Boot Images and Virtual

**Mail Stop: Amendments**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
Dear Sir:

**INFORMATION DISCLOSURE STATEMENT**

Applicants respectfully request, under 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified patent application. The present Application was filed after June 30, 2003. Therefore, under the July 11, 2003, waiver of 37 C.F.R. § 1.98(a)(2)(i) by the U.S. Patent and Trademark Office, copies of U.S. Patents and U.S. Patent Application Publications listed in the attached PTO-1449 form are not enclosed. All other references are enclosed for the convenience of the Examiner.

**REMARKS**

Pursuant to 37 C.F.R. § 1.97(b), Applicants believe this Information Disclosure Statement has been filed before the issuance/ mailing date of the first Official Action. Therefore, Applicants believe no fee is due. However, if a fee is required, the Commissioner is hereby authorized to charge any fees or credit any overpayments to Deposit Account No. 02-0384 of Baker Botts L.L.P.

Respectfully submitted,  
BAKER BOTTS L.L.P.  
Attorneys for Applicants

A handwritten signature in black ink, appearing to read 'T-TR' or similar stylized initials.

Travis W. Thomas  
Reg. No. 48,667

Date: June 14, 2006

Correspondence Address:

**Customer Number 45507**

PTO-1449  <b>Information Disclosure Citation In an Application</b>		Application No. 10/825,345		Applicant(s) Shannon V. Davidson et al.			
		Docket Number 064747.1011	Group Art Unit 2161	Filing Date April 15, 2004			
<b>U.S. PATENT DOCUMENTS</b>							
		<b>DOCUMENT NO.</b>	<b>DATE</b>	<b>NAME</b>	<b>CLASS</b>	<b>SUBCLASS</b>	<b>FILING DATE</b>
	<b>A</b>	5,682,491	10/28/1997	Pechanek et al.			
/S.D./							
	<b>B</b>	6,735,660	05/11/2004	Osten et al.			
/S.D./							
	<b>C</b>	2003/0188071	10/02/2003	Kunjan et al.			
/S.D./							
	<b>D</b>	2004/0024949	02/05/2004	Winkler et al.			
/S.D./							
	<b>E</b>	2004/0268000	12/30/2004	Barker et al.			
/S.D./							
<b>FOREIGN PATENT DOCUMENTS</b>							
		<b>DOCUMENT NO.</b>	<b>DATE</b>	<b>COUNTRY</b>	<b>CLASS</b>	<b>SUBCLASS</b>	<b>TRANSLATION</b>
							<b>YES      NO</b>
	<b>F</b>						
	<b>G</b>						
<b>NON-PATENT DOCUMENTS</b>							
		<b>DOCUMENT (Including Author, Title, Source, and Pertinent Pages)</b>					<b>DATE</b>
	<b>H</b>	Wong, William, "Switch-Chip Fuels Third-Generation InfiniBand," November 10, 2003, Electronic Design, 2 pages					11/10/2003
/S.D./							
	<b>I</b>						
	<b>J</b>						
<b>EXAMINER</b>  /Saket Daftuar/ (06/10/2008)				<b>DATE CONSIDERED</b>			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.							

U.S. PATENT AND TRADEMARK OFFICE